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APPLICANTS

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** CONTINUING DATA *-None- MDP 1/16/2006*✓ ** FOREIGN APPLICATIONS *MDP 1/16/2006*
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35 USC 119 (a-d) conditions met Verified and Acknowledged	<i>K. Imaoka</i> Examiner's Signature	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Initials			

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TITLE

Semiconductor device having a plurality of stacked semiconductor chips with positions of chip-selecting terminals being different from each other.

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